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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/635,902	08/11/2000	Kozo Harada	50090-234	8376
7590 07/27/2004				
McDermott Will & Emery 600 13th Street NW Washington, DC 20005-3096		EXAMINER CHU, CHRIS C		
		ART UNIT	PAPER NUMBER	
		2815		

DATE MAILED: 07/27/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/635,902

Applicant(s)

HARADA ET AL.

Examiner

Chris C. Chu

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 June 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 22 - 25 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☐ Claim(s) _____ is/are rejected.
- 7) ☒ Claim(s) 22 - 25 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 23 June 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Response to Amendment

1. Applicant's request filed on June 23, 2004 for reconsideration of the final rejection of the last Office action is persuasive and, therefore, the final rejection of the last Office action is withdrawn.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 22, 23 and 25 are rejected under 35 U.S.C. 102(e) as being anticipated by Oda (U.S. Pat. No. 6, 340, 845).

Regarding claim 23, Oda discloses in e.g., FIG. 1 a composite semiconductor device structure, comprising at least two semiconductor devices, wherein

- each of said semiconductor devices (1, 40 at upper and lower stages) include:
 - a semiconductor chip (1, 40);
 - at least a first electrode (2) formed on a first major surface of said semiconductor chip;

- at least a second electrode (3) formed on a second major surface of said semiconductor chip opposite to said first major surface;
- at least a conductive member (4) connecting said first electrode to said second electrode and covering a side surface of said semiconductor chip;
- said at least two semiconductor devices (at the devices 1s in the upper and lower stages) are stacked on each other (see Fig. 1); and
- Since Oda discloses in Fig. 1 a direct electrical connection by the second electrode (3) between the conductive members of lower and upper one of the semiconductor devices, Oda discloses the following limitation “a conductive member of a lower one of said semiconductor devices (1) is directly connected to a conductive member of an upper one of the semiconductor devices (1).”

Regarding claim 22, it is of note that the claim does not preclude “at least a conductive member” and a “conductive pattern” being a single structure. In other words, the claim does not specifically distinguish “at least a conductive member” as a separate structure from a “conductive pattern”. The connection pattern (4) of Oda would meet both of these structure limitations. Therefore, claimed structure is held fully met by Oda.

Regarding claim 25, it is of note that the claim does not preclude the “second electrode” and a “bump” being a single structure. In other words, the claim does not specifically distinguish “second electrode” as a separate structure from a “bump”. The soldering ball (3) of Oda would meet both of these structure limitations. Therefore, claimed structure is held fully met by Oda.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claim 24 is rejected under 35 U.S.C. 103(a) as being unpatentable over Oda (U.S. Pat. No. 6, 340, 845) in view of Lee et al. (U.S. Pat. No. 6, 222, 212).

Regarding claim 24, Oda discloses the claimed invention except for an insulating layer. Lee et al. teaches in e.g., Fig. 8A an insulating layer (804) formed on a second major surface (a surface that has elements 822 and 824) of a semiconductor chip (801) opposite to a first major surface (see Fig. 8A). Thus, it would have been obvious to one of ordinary skill in the art at the time when the invention was made to modify Oda by using the conductive line pattern as taught by Lee et al. The one of ordinary skill in the art would have been motivated to modify Oda in the manner described above for at least the purpose of protecting the semiconductor device from moistures in the air.

Response to Arguments

6. Applicant's arguments, see pages 5 and 6, filed on June 23, 2004, with respect to the rejections of claims 23 and 24 under 35 U.S.C. § 102 and the claim 22 under 35 U.S.C. § 103 have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of the new

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ground of rejection, e.g., different interpretation of the previously applied reference and newly found prior art reference.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chris C. Chu whose telephone number is 571-272-1724. The examiner can normally be reached on 11:30 - 8:00.

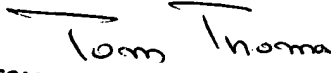
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 517-272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Chris C. Chu
Examiner
Art Unit 2815

c.c.
7/21/04 7:34:57 PM


TOM THOMAS
SUPERVISORY PATENT EXAMINER
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